UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,730,515 B1 Page 1 of 2

APPLICATION NO. : 09/239907 DATED : June 1, 2010

INVENTOR(S) : Andrew MacCormack et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Columns 19 & 20, Line 18-31

Claim 23, "A receiver for processing a packetized digital data stream, the receiver comprising: an input module to receive and process a data packet; a memory; a receiver processor to control storage of desired packet identifiers and associated control information in the memory; and a transport controller having a transport processor to extract a packet identifier from a packet in the input module and a search engine to search the memory for a match of the extracted packet identifier to a desired packet identifier stored in the memory, wherein responsive to a match the transport processor retrieves from the memory control information associated with the desired packet identifier stored in the memory and controls processing of the received data packet by the input module based on the retrieved control information; wherein the transport processor generates a control signal to control processing of a packet by the input module based on associated control information retrieved from the memory; wherein the input module passes a data payload to the transport controller in response to the control signal." should read as --The receiver of claim 20 wherein the input module passes a data payload to the transport controller in response to the control signal.--.

Column 20, Line 7-30

Claims 24, "A receiver for processing a packetized digital data stream, the receiver comprising: an input module to receive and process a data packet; a memory; a receiver processor to control storage of desired packet identifiers and associated control information in the memory; and a transport controller having a transport processor to extract a packet identifier from a packet in the input module and a search engine to search the memory for a match of the extracted packet identifier to a desired packet identifier stored in the memory, wherein responsive to a match the transport processor retrieves from the memory control information associated with the desired packet identifier stored in the memory and controls processing of the received data packet by the input module based on the retrieved control information; wherein the transport processor generates a control signal to control processing of a packet by the input module based on associated control information retrieved from the memory; wherein the input module passes a data payload to the transport controller in response to the control signal; wherein the transport

Signed and Sealed this Eleventh Day of January, 2011

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Director of the United States Patent and Trademark Office

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controller reformats the data payload based on the control information and passes the reformatted data payload to the input module for output in an alternative output stream." should read as --The receiver of claim 23 wherein the transport controller reformats the data payload based on the control information and passes the reformatted data payload to the input module for output in an alternative output stream.--.